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**For Immediate Release**

## **Cypress Announces Synplicity Support For Delta39K™ CPLDs**

### ***Enabling Smooth Integration between Synplify® and Warp™ Software***

SAN JOSE, California, August 4, 2000 — Cypress Semiconductor Corporation (NYSE:CY) today announced that designers can use Synplicity's Synplify® Version 6.0, VHDL and Verilog synthesis tool, to design with Cypress's Delta39K™ CPLDs. Cypress's Warp™ Release 6.0 seamlessly integrates the Synplify environment with Cypress's popular Warp development tools, providing Synplicity's Synplify customers with access to the Cypress's newest family of high-performance silicon in Synplify's familiar, device-independent environment.

The new capabilities extend Synplicity's support for Cypress CPLDs, which previously included Cypress's Ultra37000™ CPLD family. Synplicity is a member of the Cypress Federation, a coalition of third-party design tool providers that support Cypress CPLDs.

The integration of Synplify and Warp allows customers to enter designs in the Synplify environment, which leverages Synplicity's Behavior Extracting Synthesis Technology™ (B.E.S.T.™) algorithms to achieve optimum results. The resulting netlist then can be read directly into Cypress's Warp environment, which provides fitting and mapping capabilities developed for Cypress CPLDs.

"Synplicity's large installed customer base benefits from its synthesis technology in a familiar environment while having access to the fitting and mapping algorithms developed by Cypress specifically for the Delta39K family," said Geoff Charubin, marketing director for programmable logic at Cypress. "The seamless flow offers Synplicity customers more flexibility in terms of device choice and access to the largest CPLDs in the world."

"Continuing our commitment to provide the broadest support of PLD vendors, we are pleased to add support for the Delta 39K," said Joe Gianelli, channel marketing director at Synplicity. "Additionally, the integration of Synplify and Cypress's Warp Release 6.0 has already received positive customer feedback, and we are optimistic that this solution will enable new and current users to quickly and efficiently synthesize their Cypress devices for optimum performance."

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“Synplicity is a great addition to the Cypress Federation of third-party tool providers that offer support for Cypress CPLDs,” said Christopher Norris, vice president of Cypress's Programmable Logic Division. “Synplicity synthesis tool support is a critical element to the success of any CPLD offering. In addition, Cypress gains increased market exposure and visibility for our flagship CPLD family.”

Customers will require a copy of the Synplify software, available at <http://www.synplicity.com>, and a copy of Cypress's *Warp* software, available at <http://www.cypress.com/pld/warp.html>. More details are available at <http://www.cypress.com/pld/synplicity>.

### **About Synplify**

First introduced in 1995, Synplicity's Synplify synthesis tool represents a new breed of technology, designed independently of existing academic or commercial code, and features the company's innovative B.E.S.T. algorithms. The tool accepts industry-standard Verilog and VHDL descriptions, and produces optimized implementations for programmable devices from many leading vendors. Designed to deliver the highest quality of results, Synplify is also extremely fast and easy-to-use. It includes a built-in language-sensitive editor and optional graphical (block diagram) analysis tool that gives direct feedback for fast design debug.

### **Broadest CPLD Offering**

Cypress offers a broad range of programmable logic, including industry-standard SPLDs such as the 16V8, 20V8 and 22V10, and the In-System Reprogrammable™ (ISR™) Ultra37000 and FLASH370i™ CPLDs. Cypress's Delta39K CPLDs span from 15,000-to-350,000 gates, many times larger than any other CPLD, and are the first CPLDs to offer embedded memory elements, including optimized FIFO and dual-port memory blocks. Cypress's devices are supported by *Warp* design tools, giving users access to a broad range of devices with a single, open development tool.

### **About Cypress**

Cypress Semiconductor provides high-performance integrated circuit solutions “By Engineers. For Engineers.™” for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multiport and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

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More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 3,900 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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